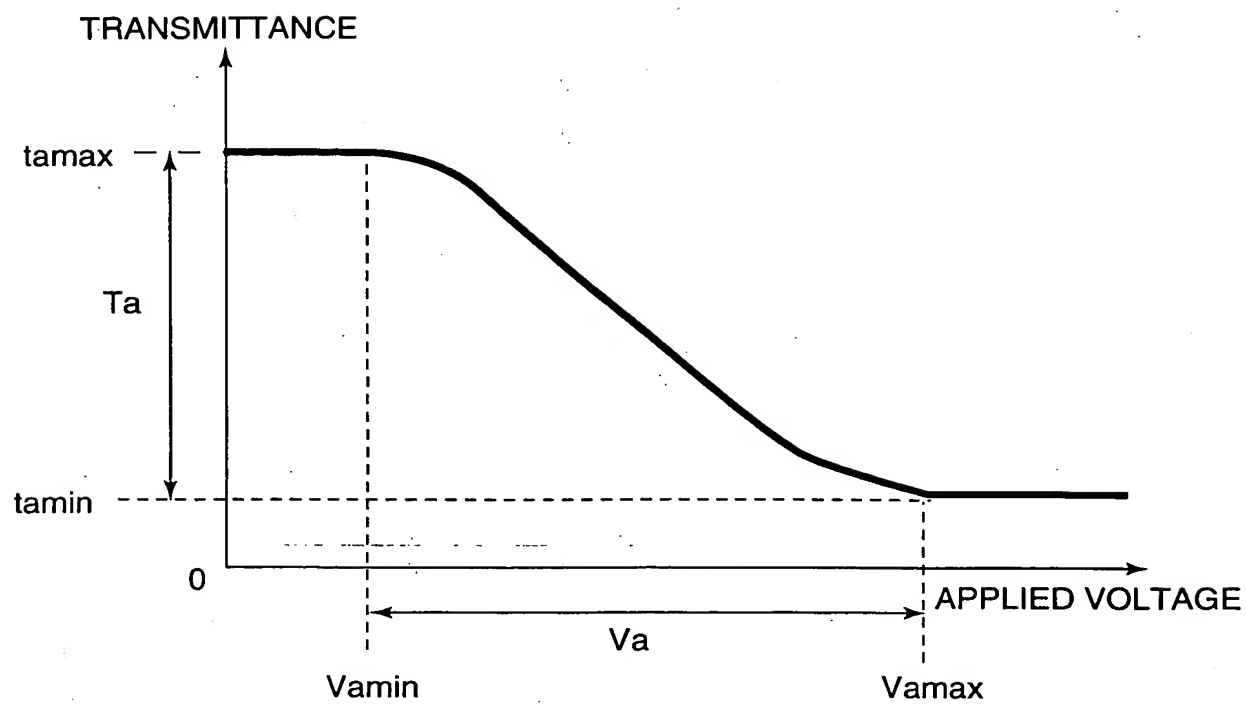


10050831-011802

(a)



(b)

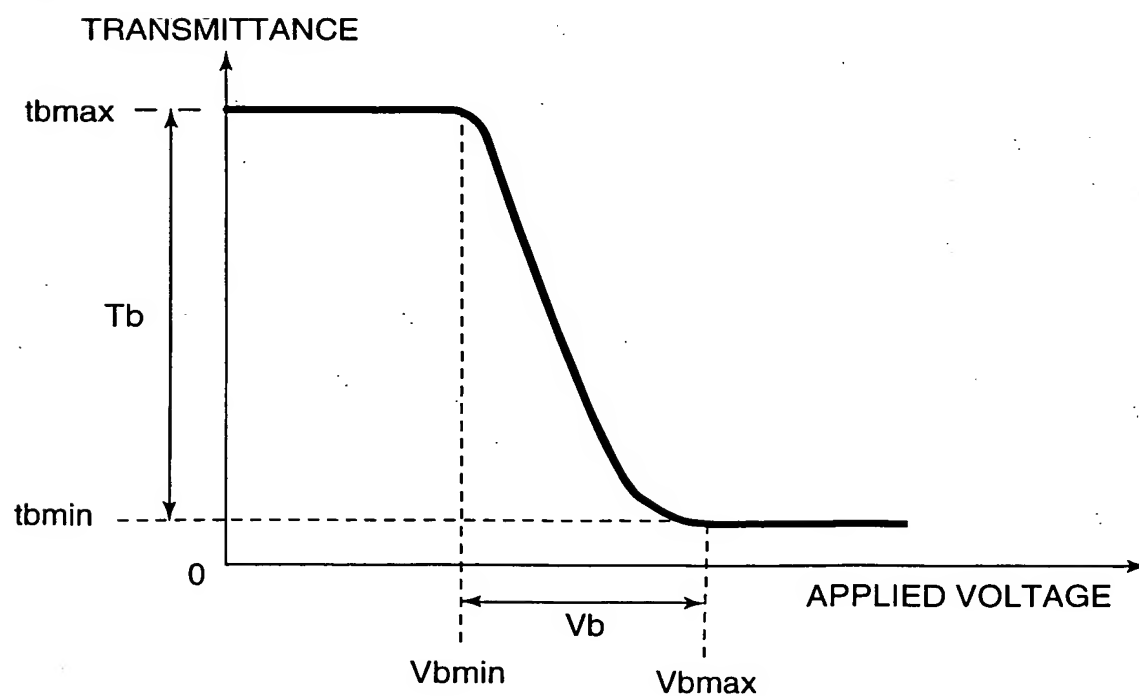


FIG. 2

100A LIQUID-CRYSTAL DISPLAY PANEL

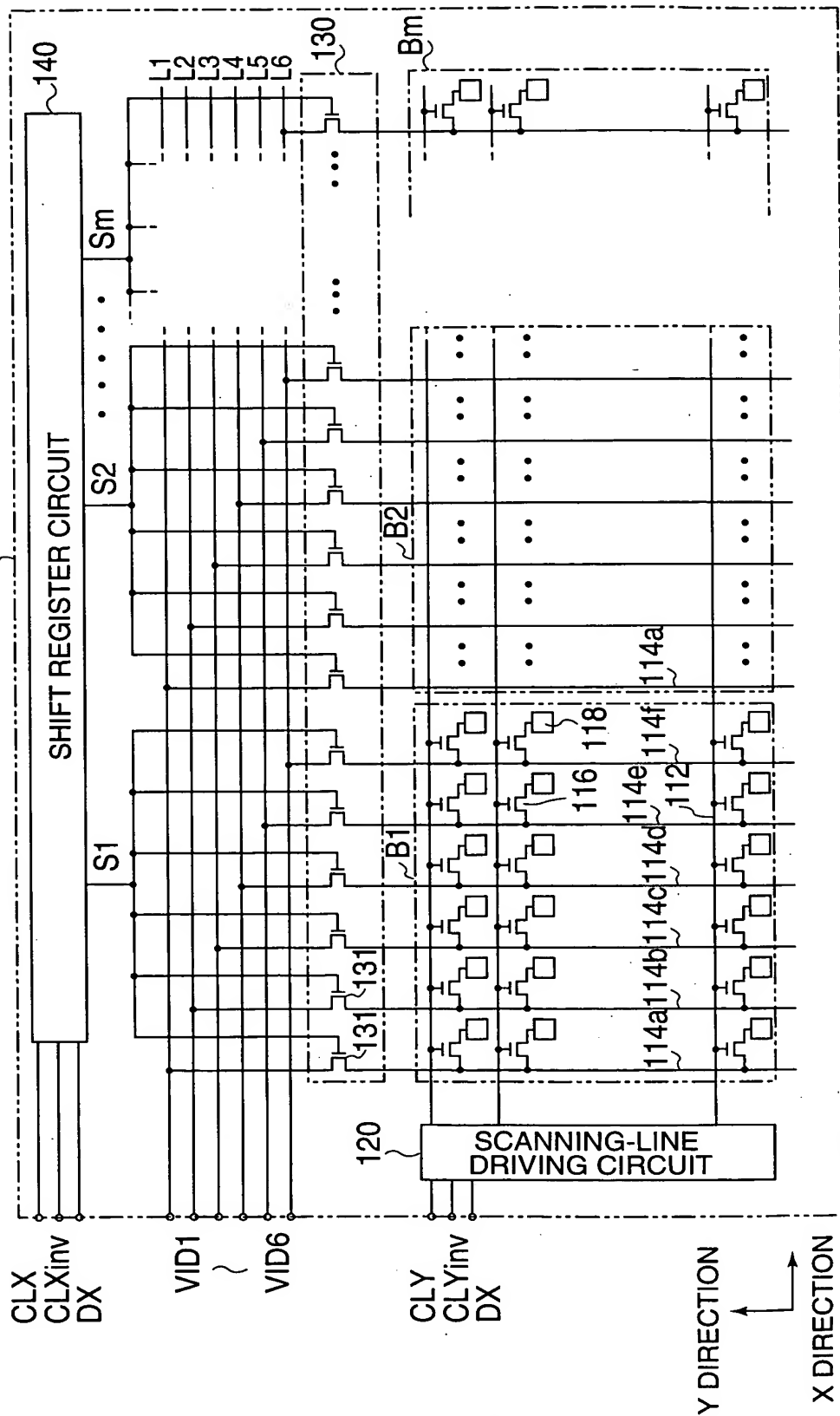


FIG. 3

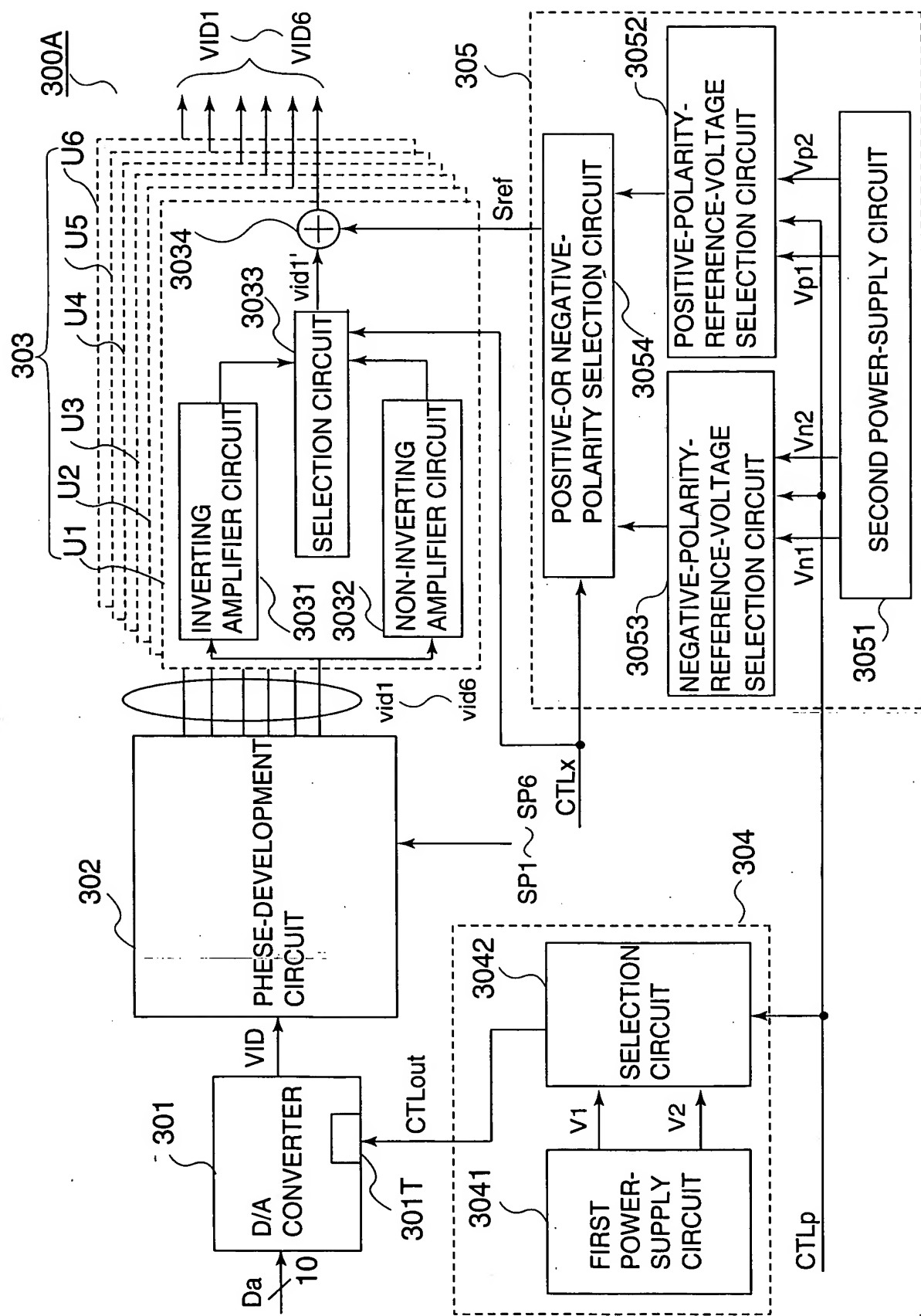


FIG. 4

2025031.01300

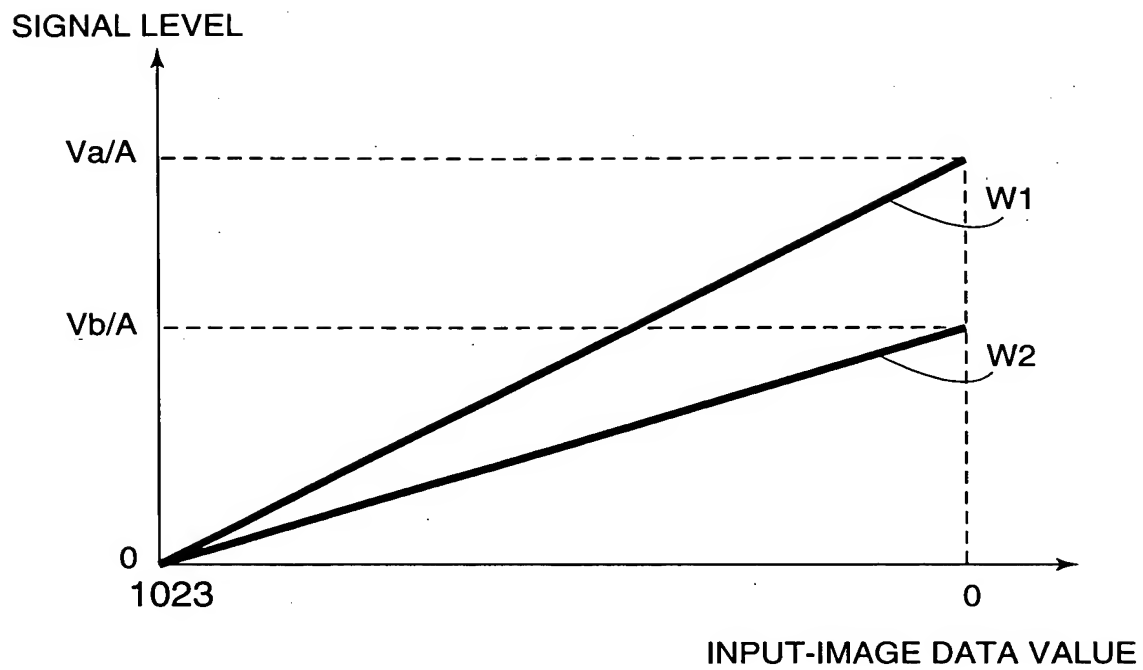


FIG. 5

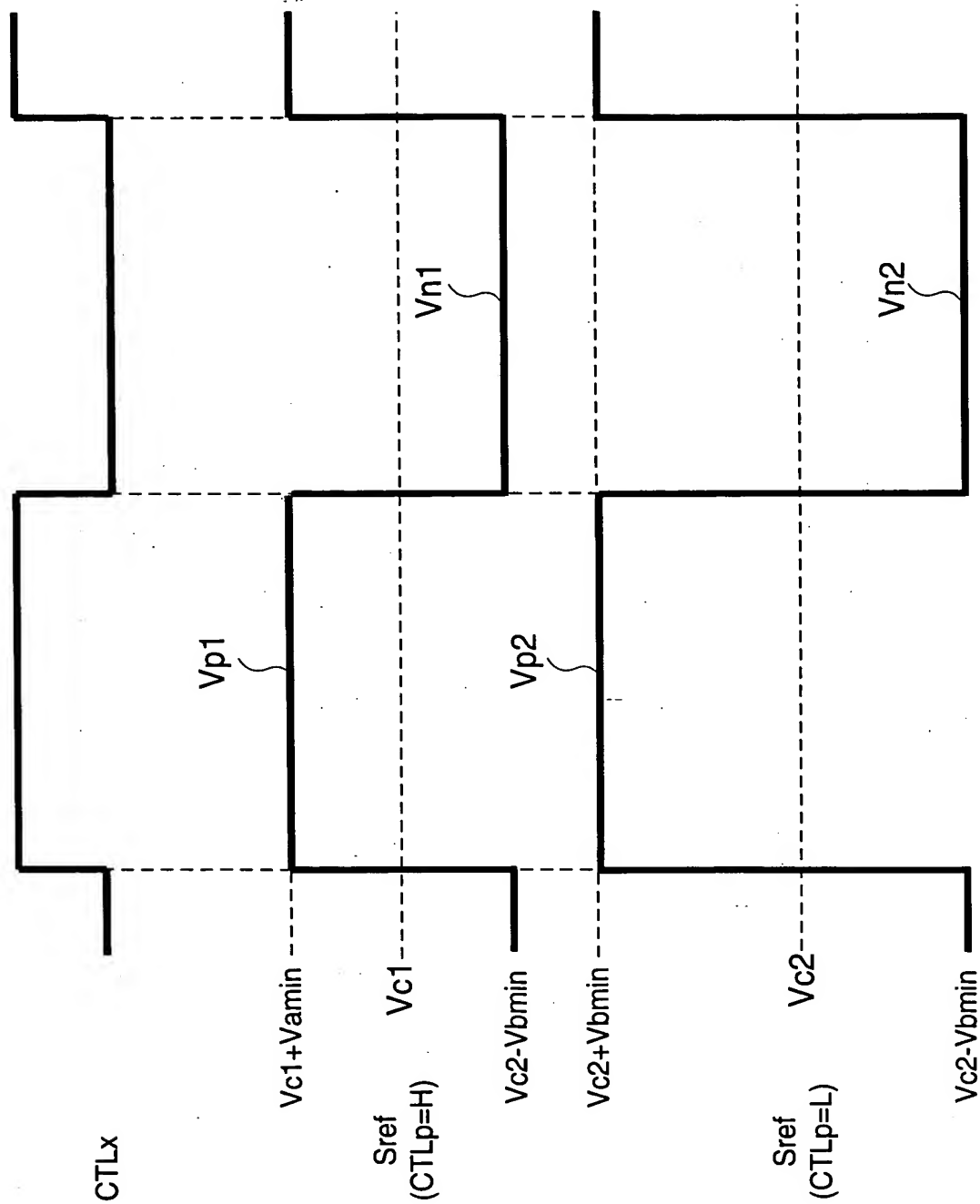
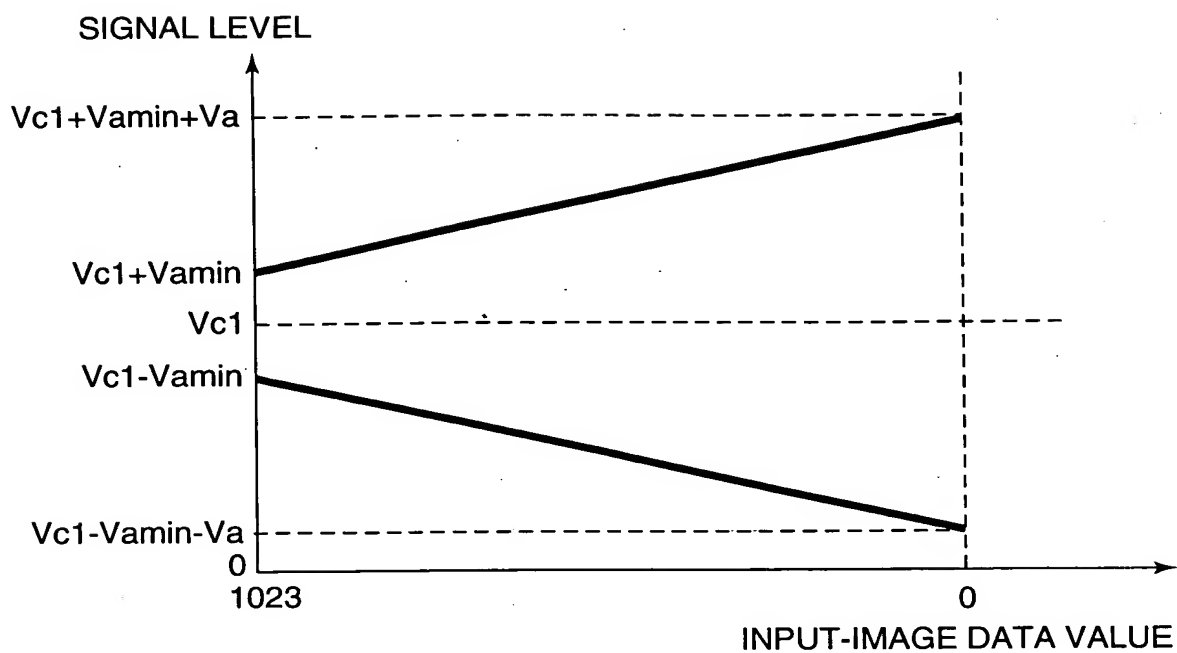


FIG. 6

(a)



(b)

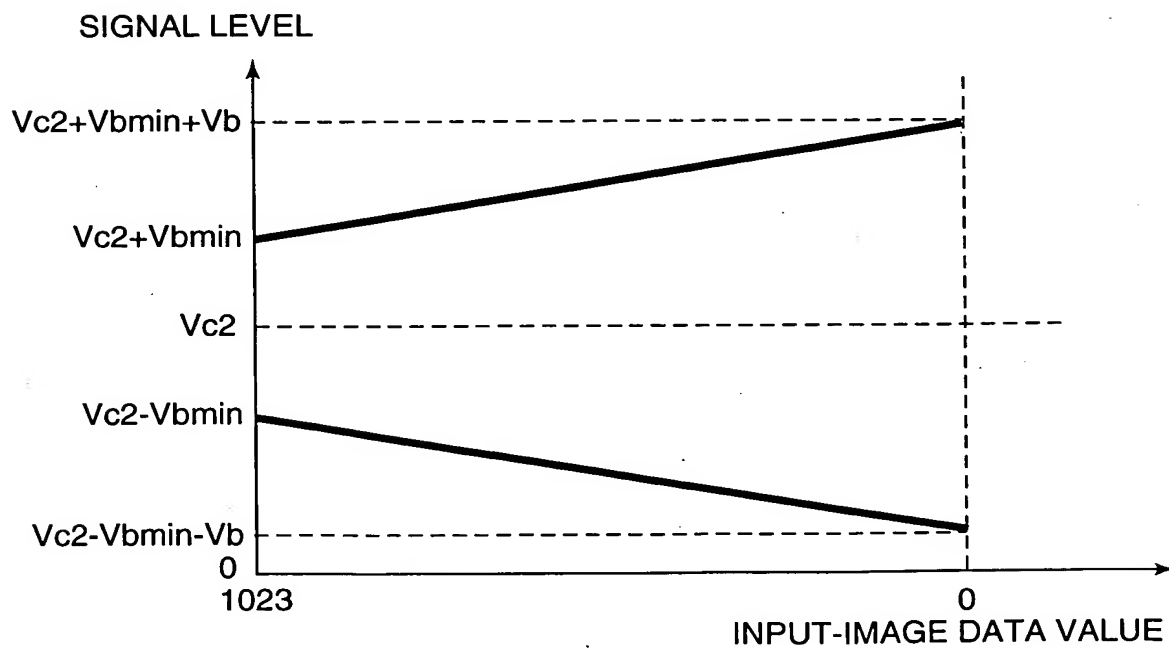


FIG. 7

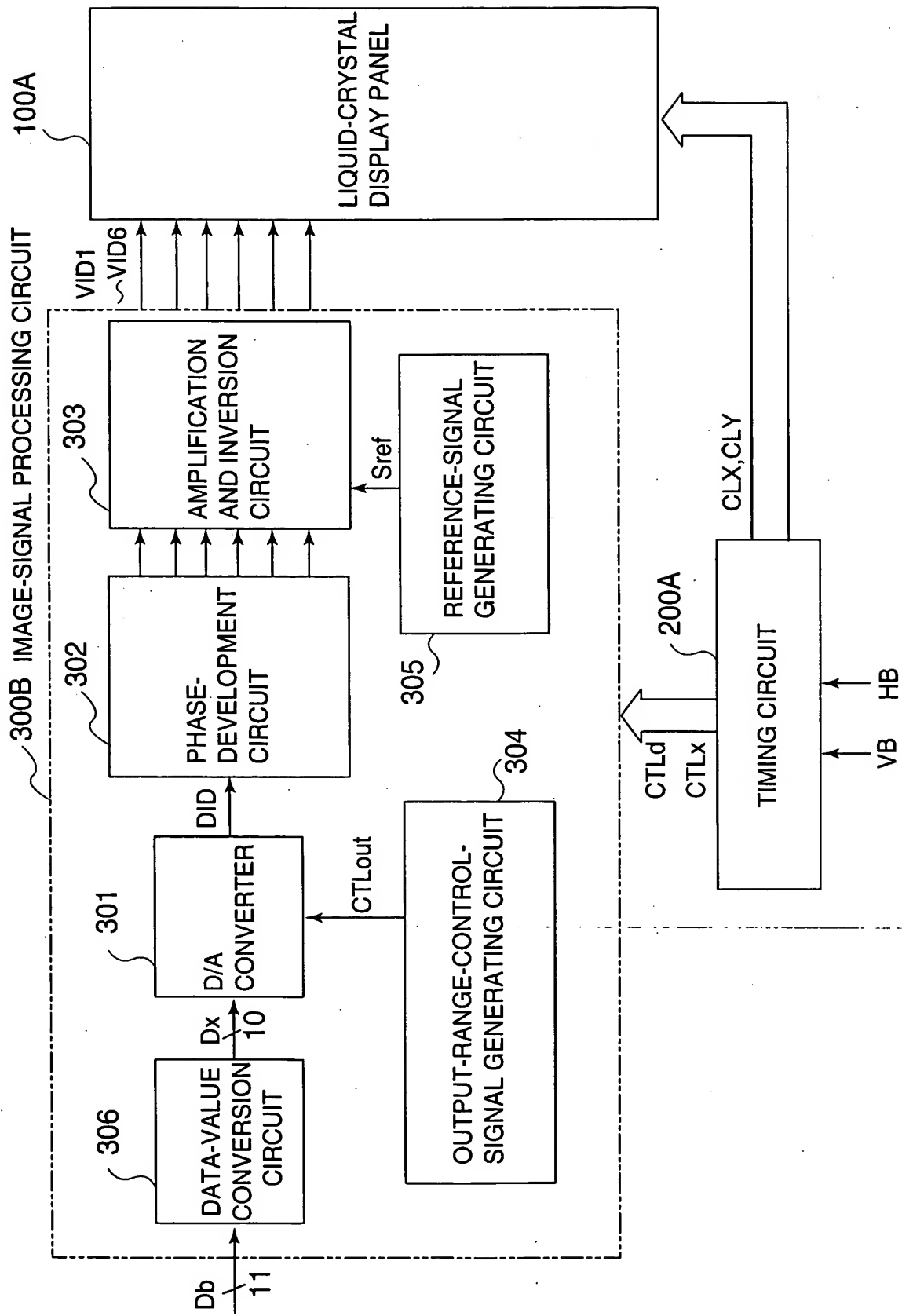
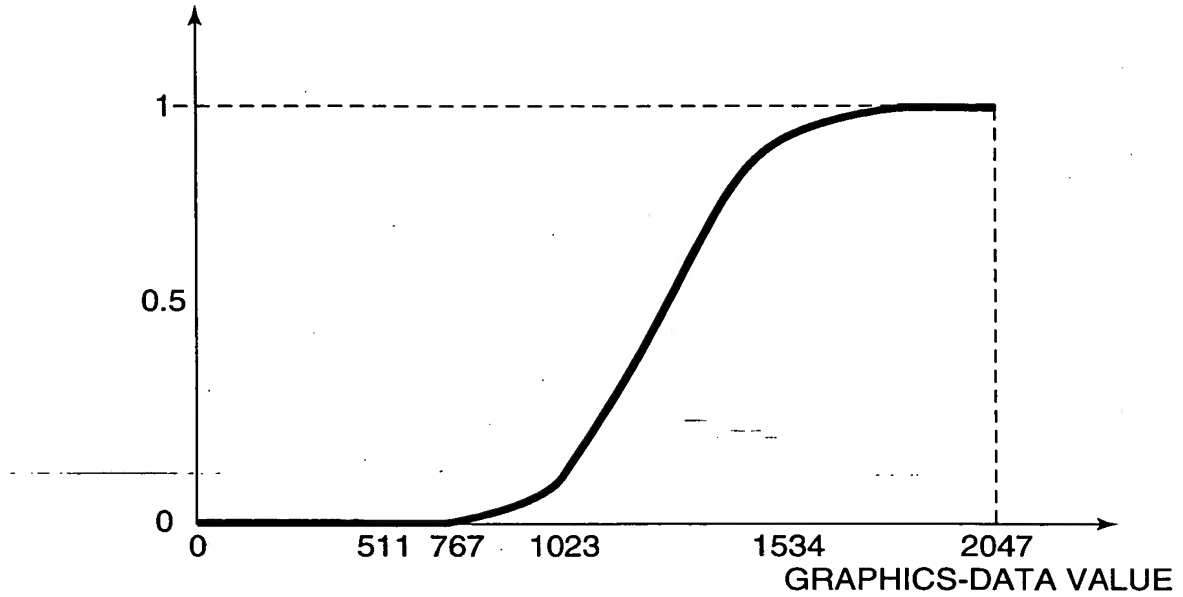


FIG. 8

(a)

PROBABILITY DENSITY



(b)

PROBABILITY DENSITY

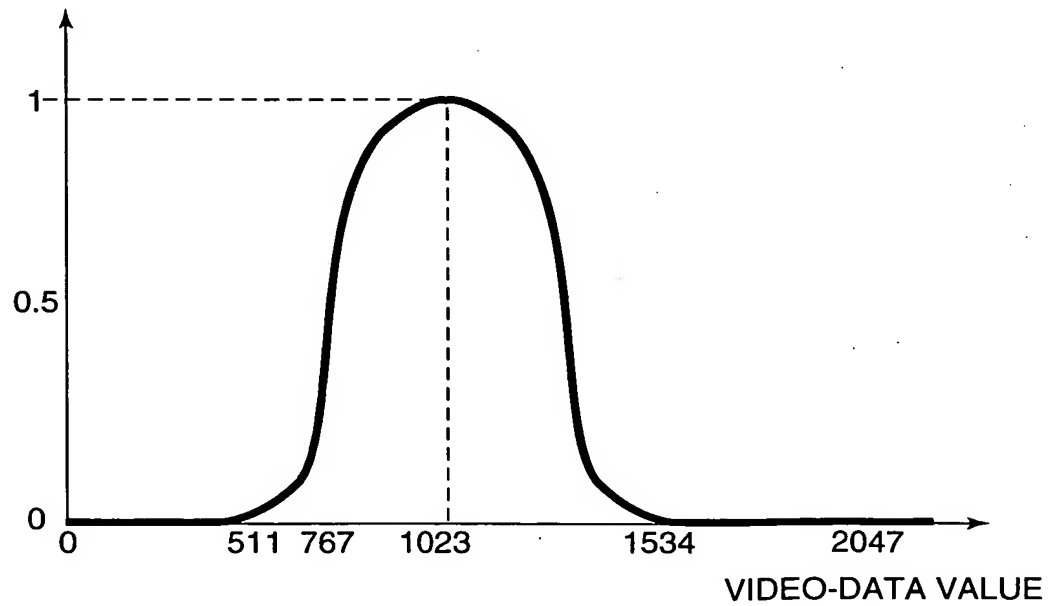


FIG. 9

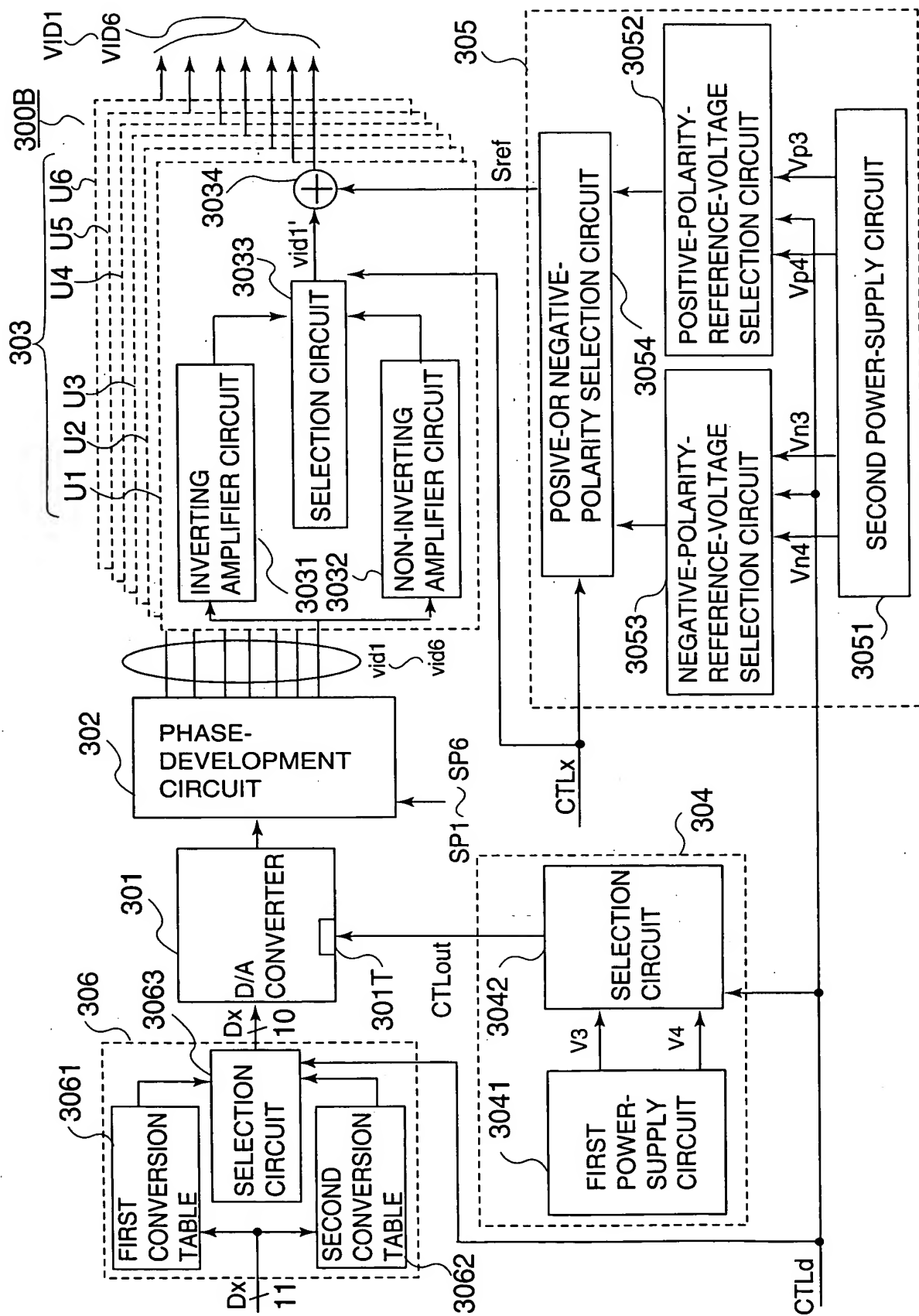
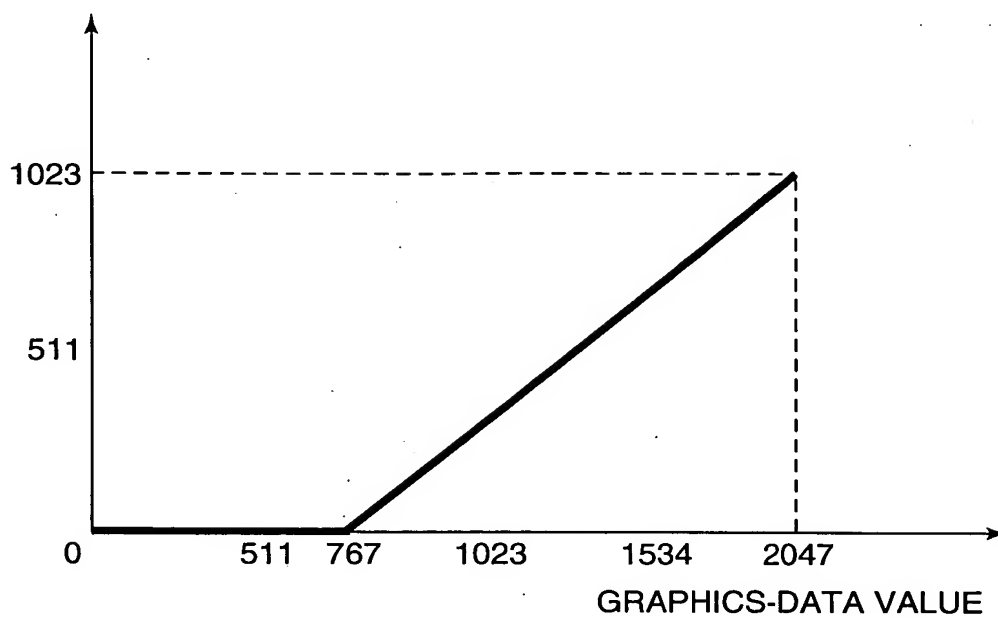


FIG. 10

(a)

FIRST-CONVERSION DATA VALUE



(b)

SECOND-CONVERSION DATA VALUE

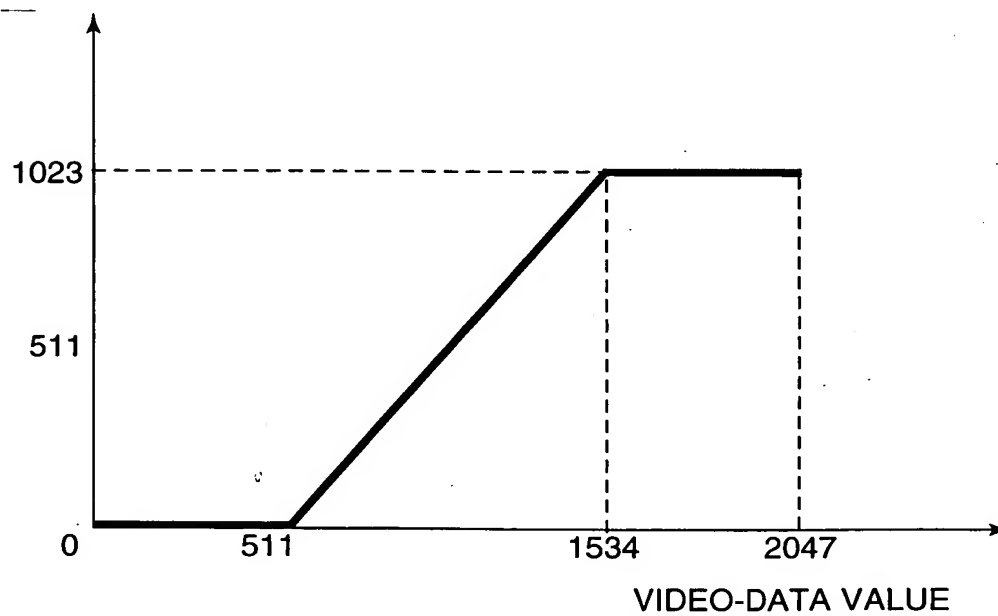


FIG. 11

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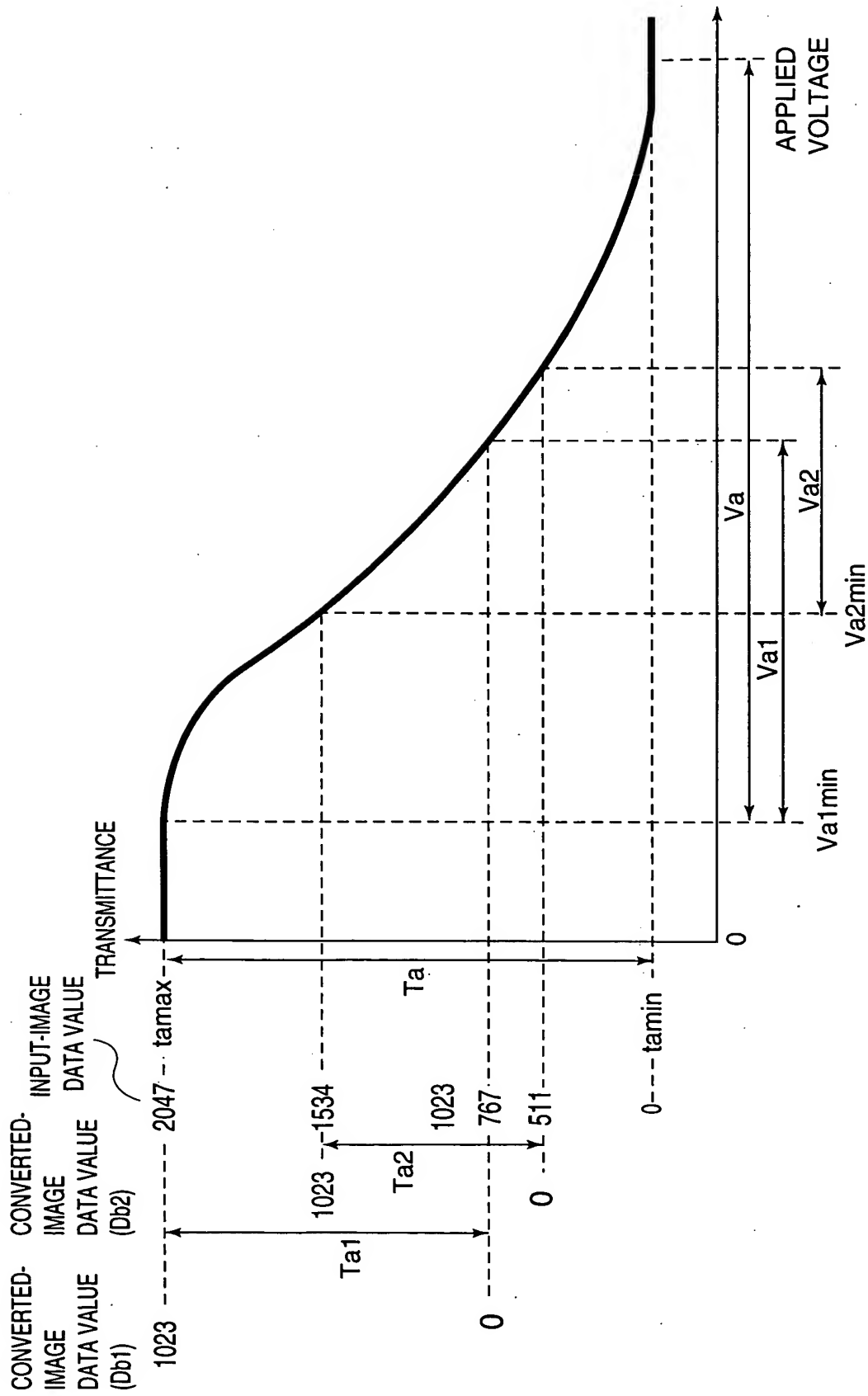


FIG. 12

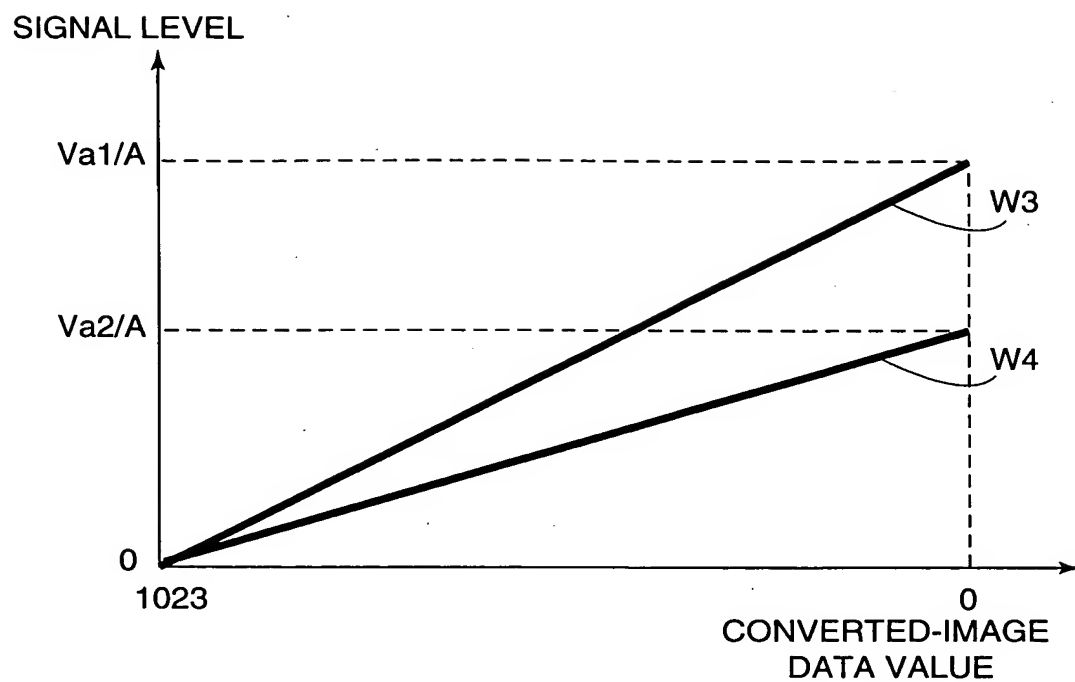


FIG. 13

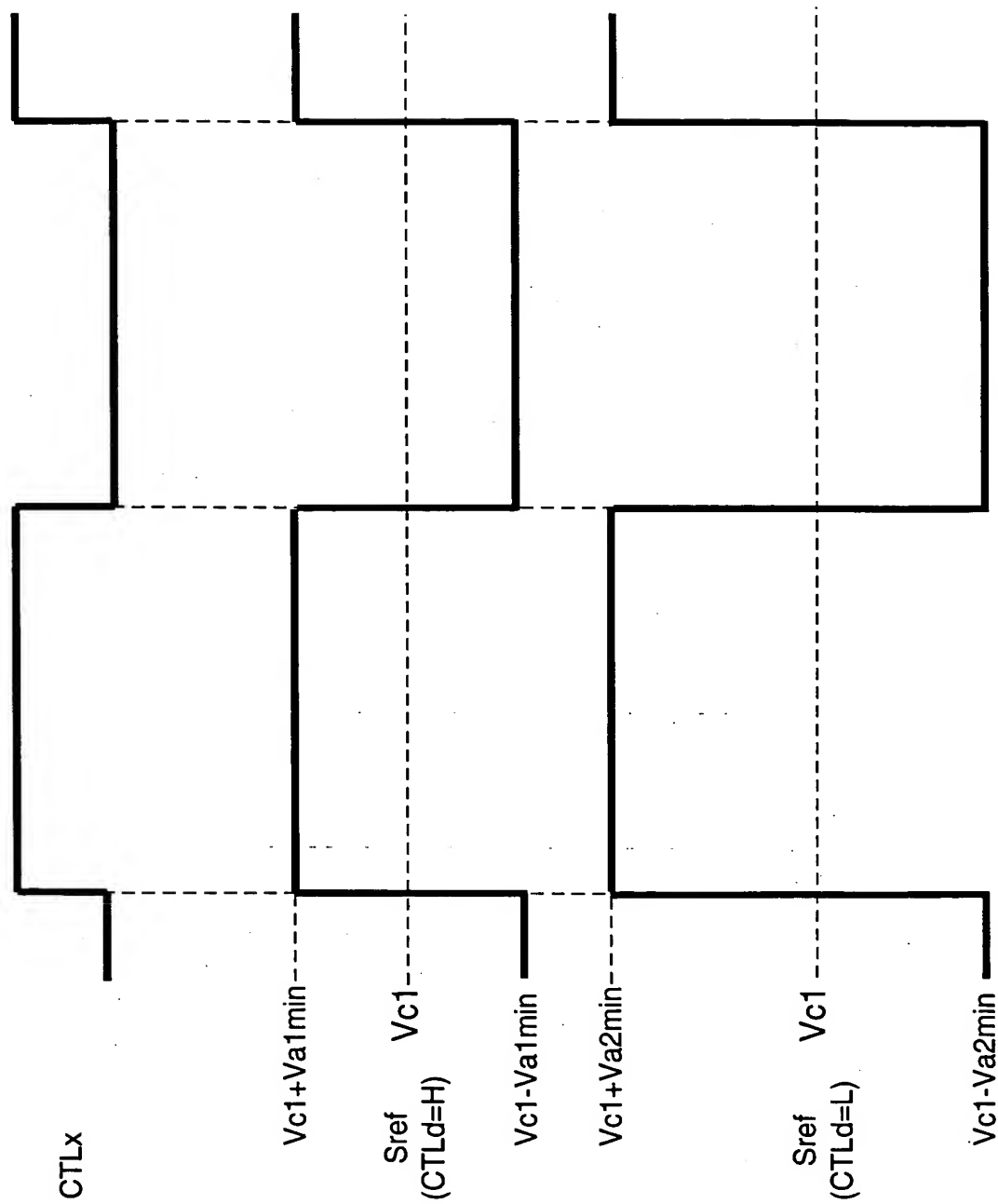
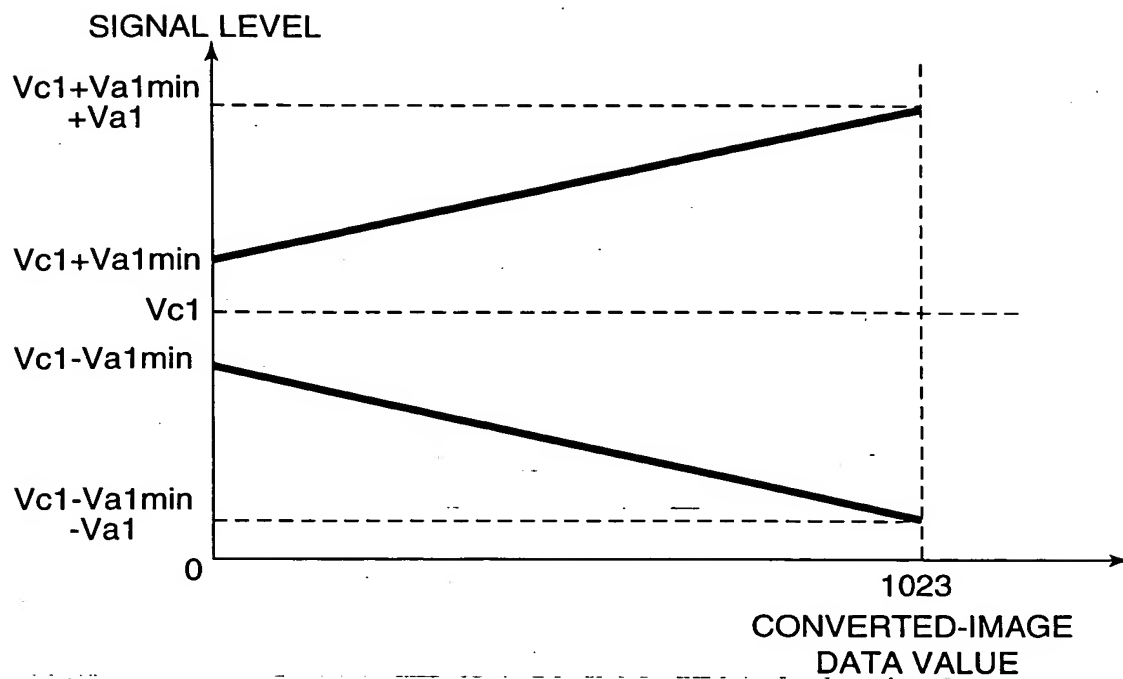


FIG. 14

(a)



(b)

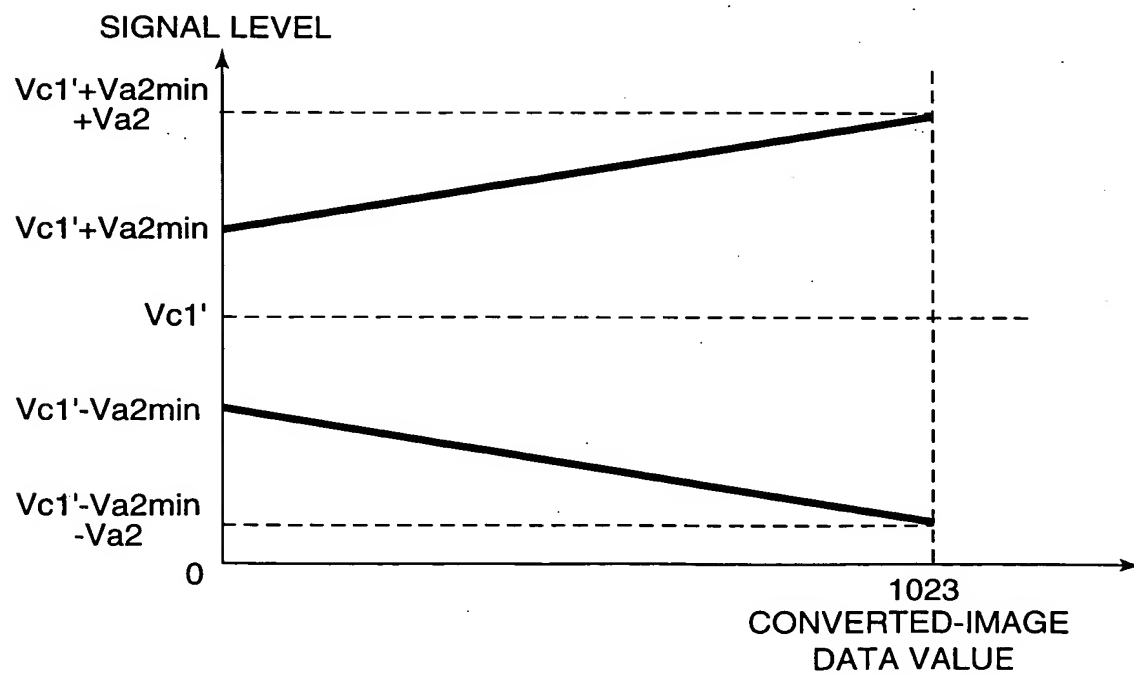


FIG. 15

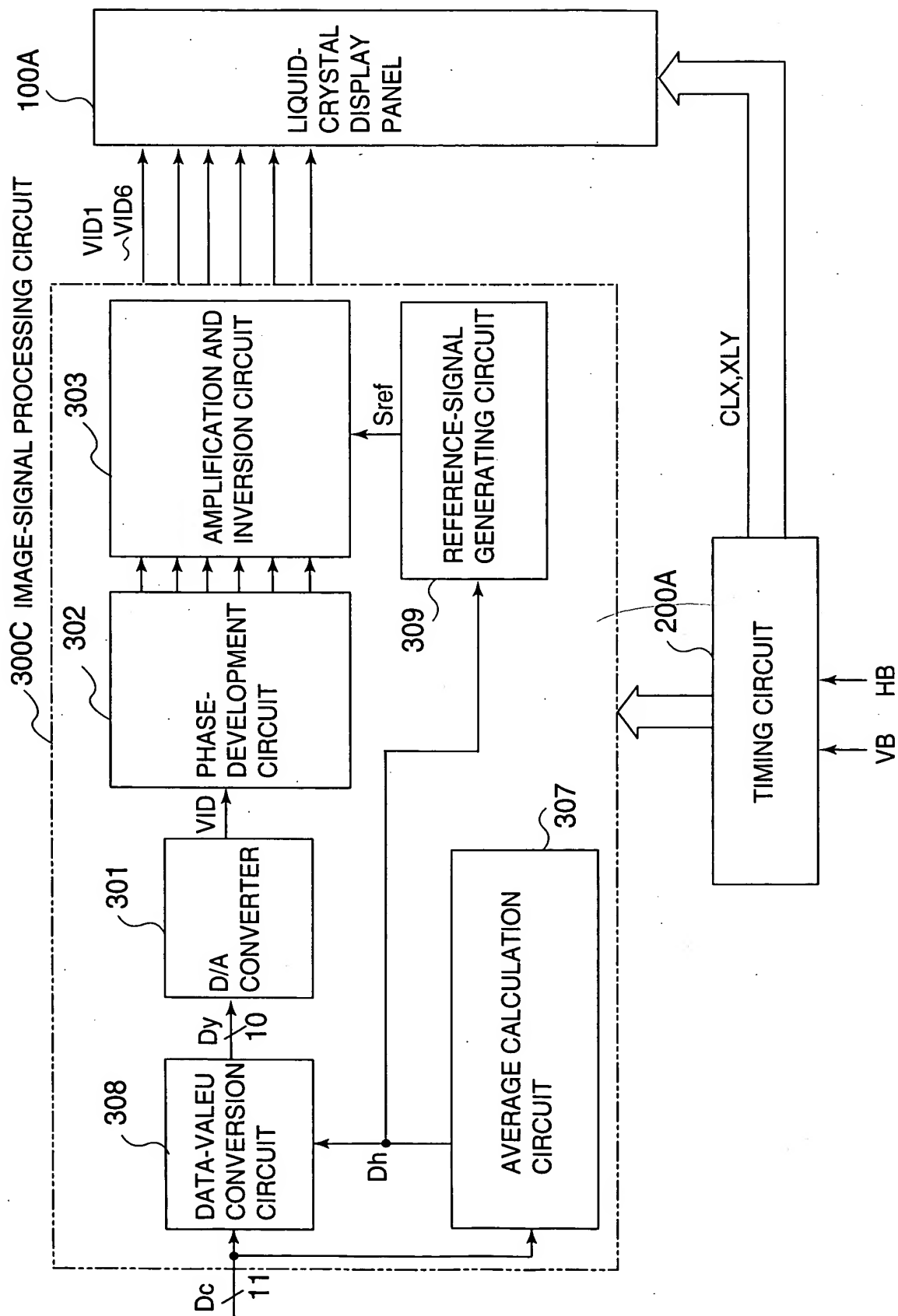


FIG. 16

PROBABILITY DENSITY

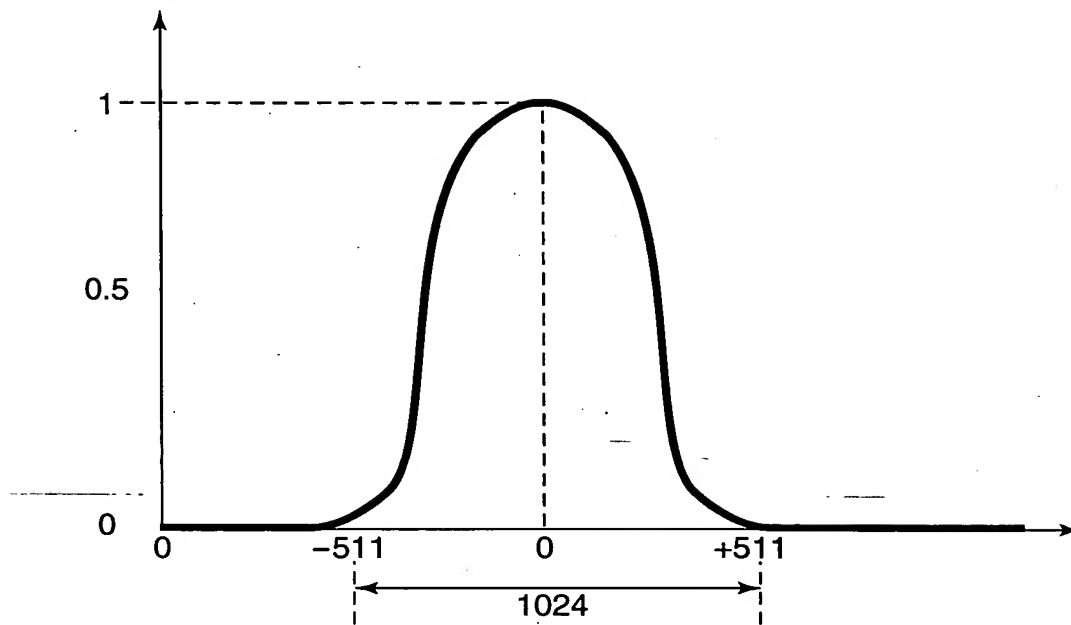


FIG. 17

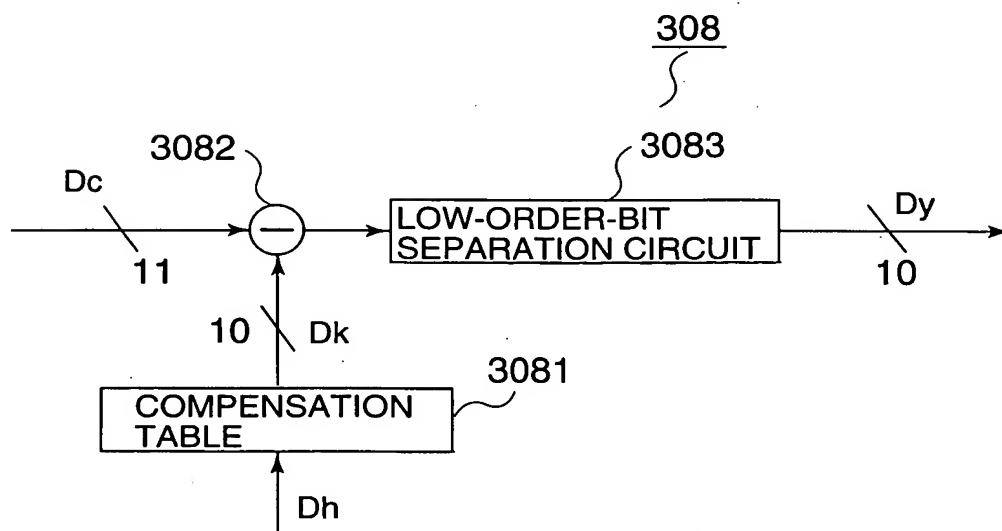


FIG. 18

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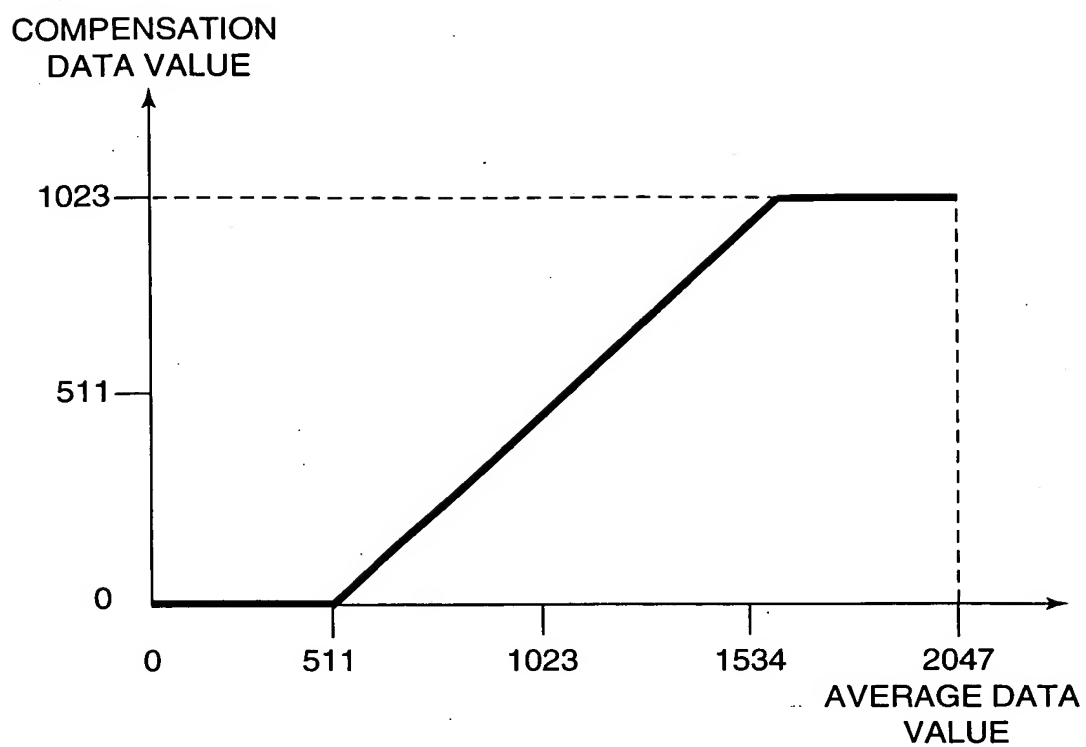


FIG. 19

The diagram shows a system 309 for generating a minimum-applied voltage. It includes a block 3091 labeled "MINIMUM-APPLIED-VOLTAGE GENERATING CIRCUIT" which receives an input D_h and outputs V_{min} . This output is fed into two summing junctions, 3092 and 3093. Summing junction 3092 also receives a feedback signal V_p and outputs V_n . Summing junction 3093 also receives a feedback signal V_n and outputs V_p . A reference voltage V_{c1} is connected to the inputs of both summing junctions. The outputs V_p and V_n are fed into a block 3094 labeled "POSITIVE-OR-NEGATIVE-POLARITY SELECTION CIRCUIT". This block also receives a control signal $CTLx$ and produces the final output S_{ref} .

FIG. 21

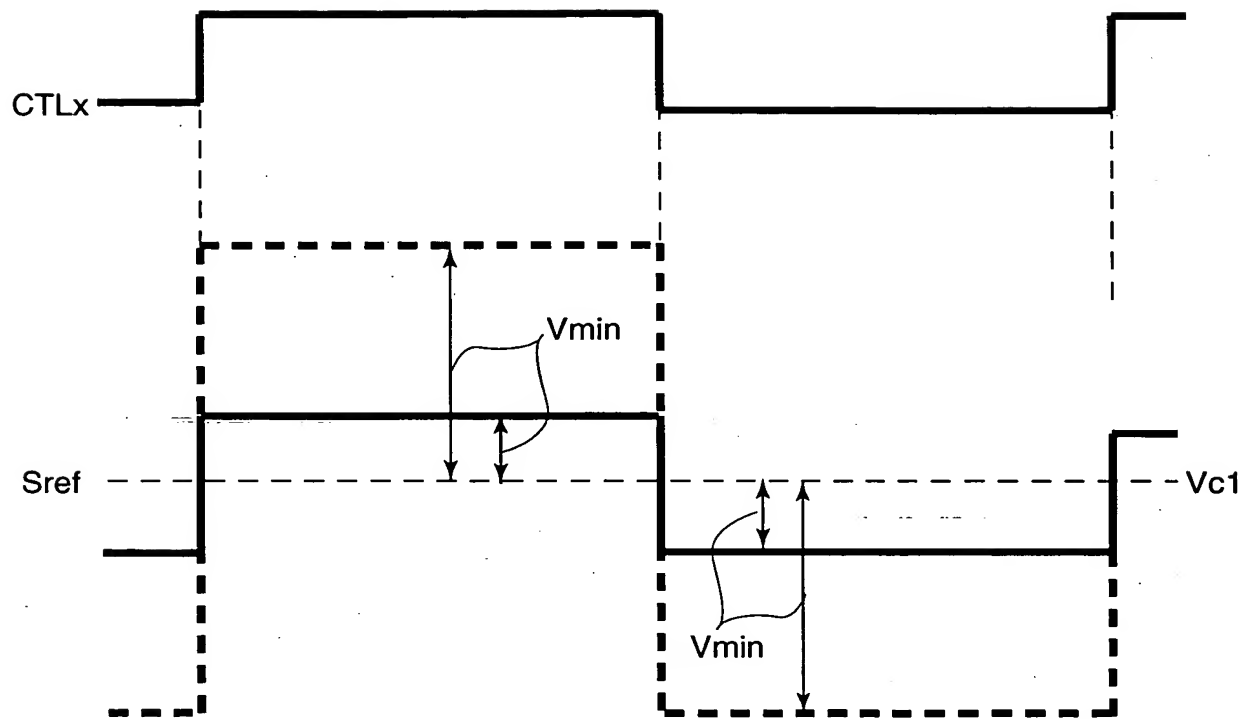
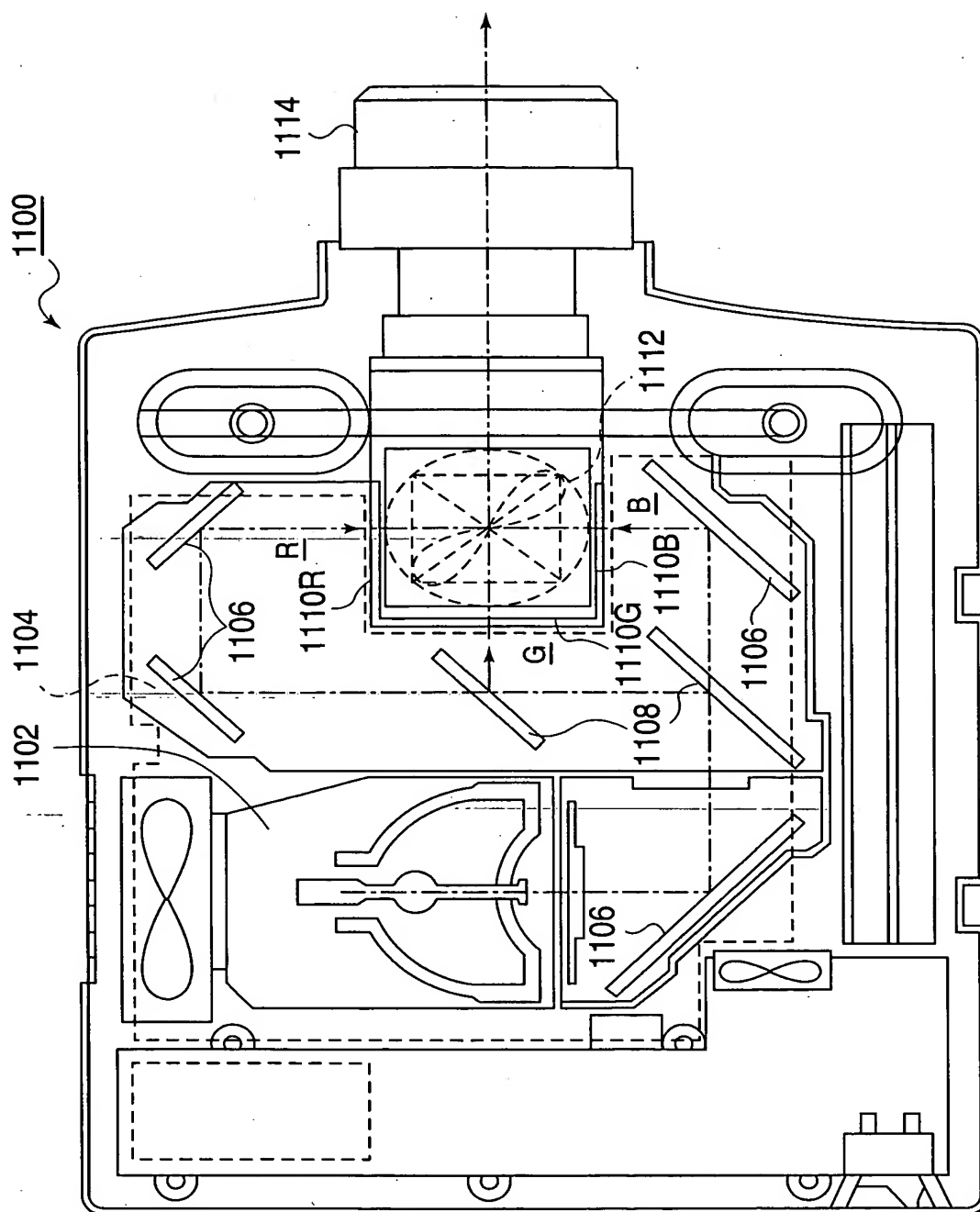


FIG. 22



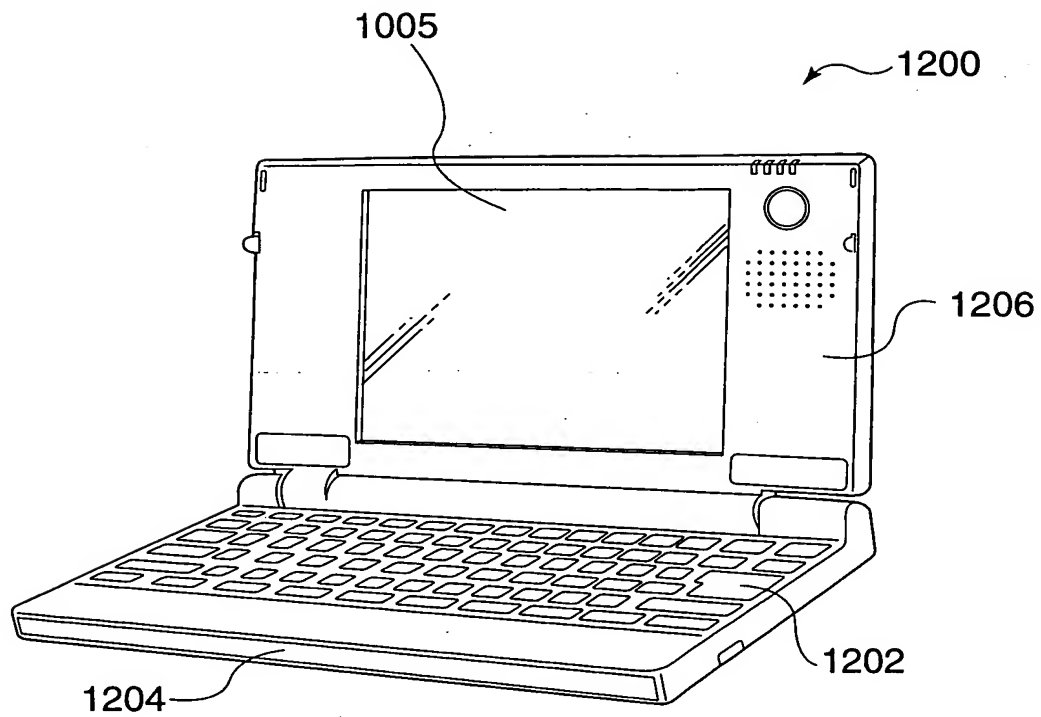


FIG. 25

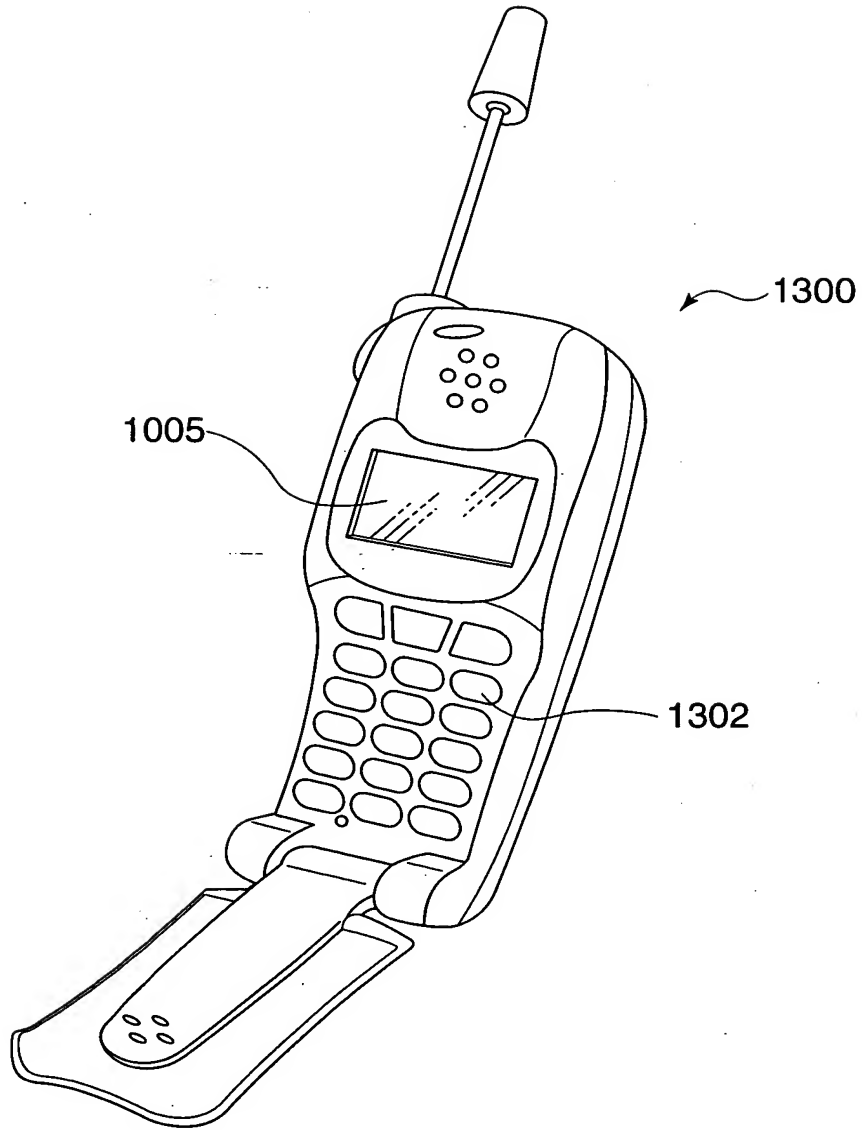


FIG. 26

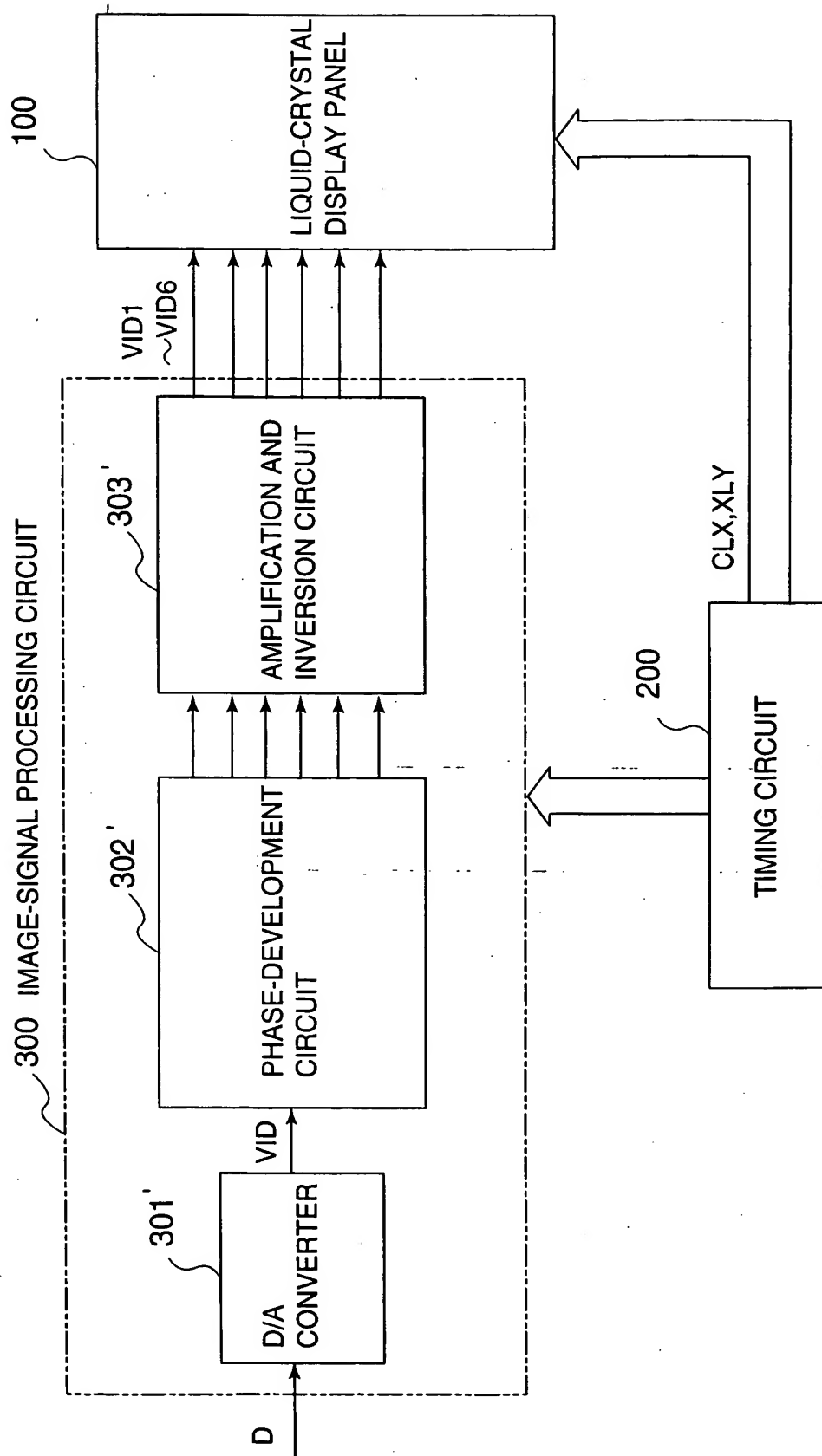
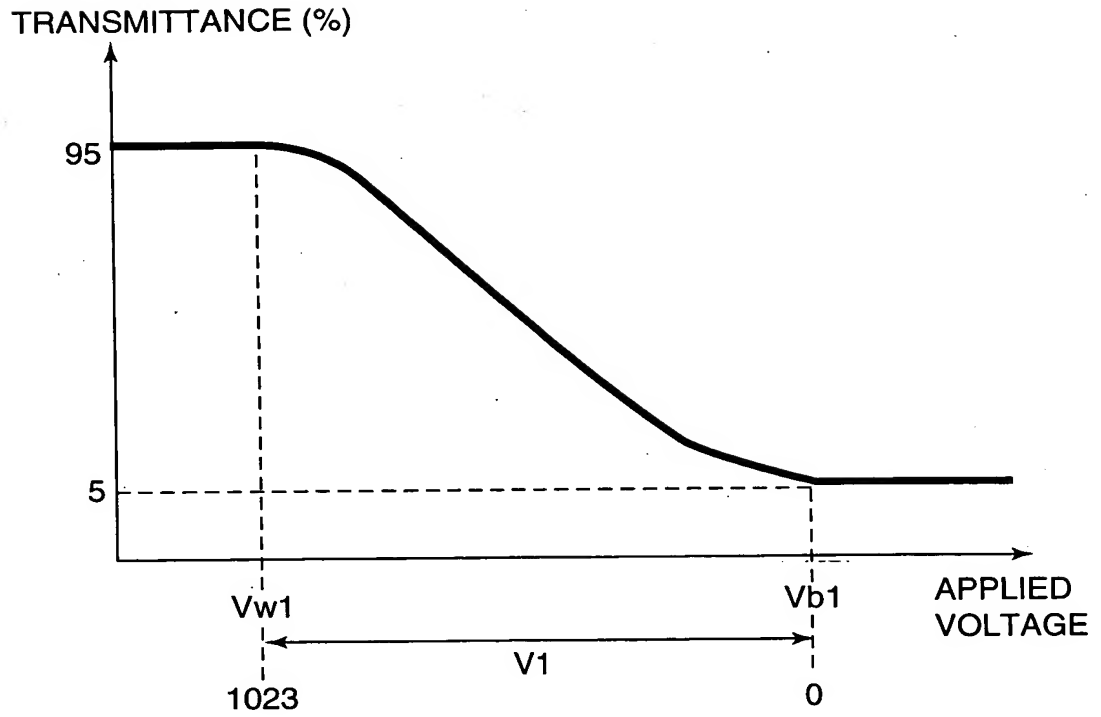


FIG. 27

(a)



(b)

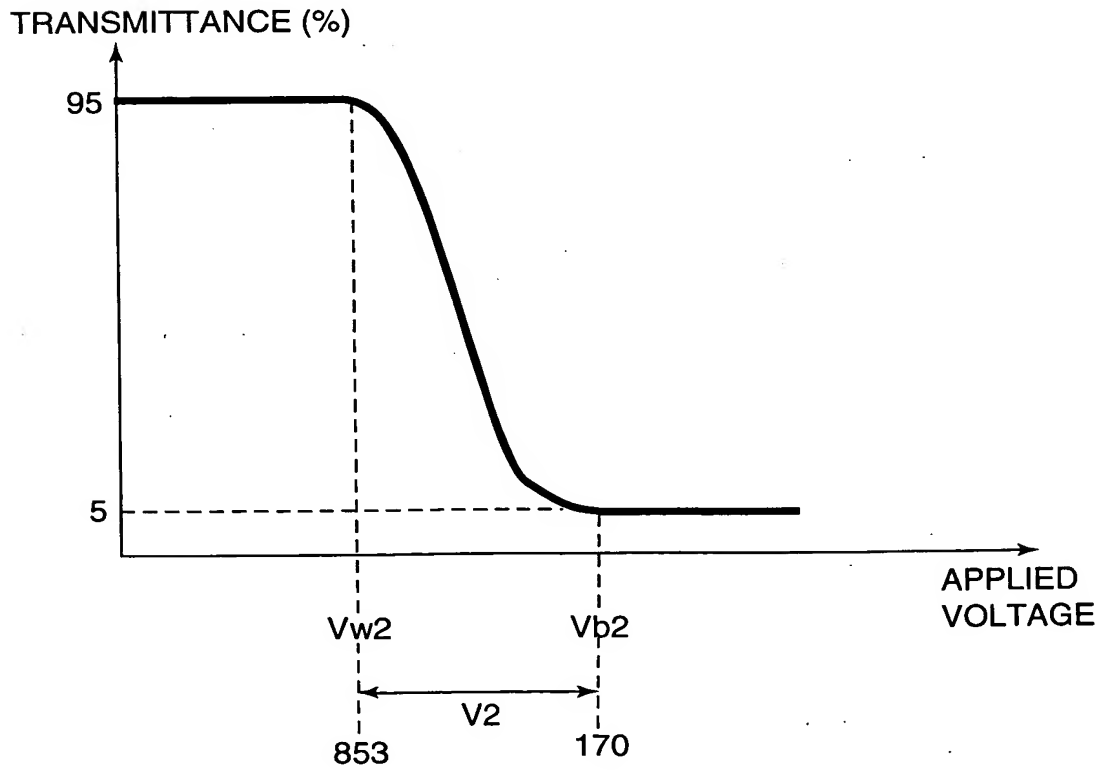


FIG. 28